

**AMENDMENTS TO THE SPECIFICATION**

Replace the paragraph beginning on page 15, line 3, with the following:

While the present embodiment provides both a configurable modem processor 102a and a configurable codec processor 104 in communication device 100, the present invention is well suited to an alternative configuration that uses a configurable modem processor 102a with a conventional codec processor (e.g., a digital signal processor), and to using a configurable codec processor 104 with a conventional configurable modem processor. Lastly, while the present invention provides a single modem processor, with a single optional configurable modem processor 102b, the present invention is modular, and thus is well suited to using a wide range of processor types and quantities, as appropriate for a given spread spectrum application. Communication device can also include a configuration that accommodates multiple communication standards. For example, the present invention can be applied to communication protocols such as orthogonal frequency division multiplexing (OFDM). More detail is provided in a commonly assigned and related application, which is incorporated herein by reference and entitled "METHOD AND APPARATUS TO SUPPORT MULTI STANDARD, MULTI SERVICE BASE-STATIONS FOR WIRELESS VOICE AND DATA NETWORKS," Attorney Docket No. 9824-0035-999, US patent application serial number \_\_\_\_\_, 09/752,050, filed on December 2829, 2000.

Replace the paragraph beginning on page 21, line 1, with the following:

The programmers model and API of Figure 1D also provides efficient use of hardware parallelism. Thus, the present invention provides a method and architecture that overcomes the challenging task of scheduling the many hardware resources in the complete system. This requires an efficient mechanism for communication between the hardware resources, both within a configurable processor, e.g., within configurable modem processor 102a of Figure 1B, and between the configurable processors (e.g., between configurable modem processor 102a/codec processor 104 and the controlling processors, e.g., processor 112, BTS card controller 110a or 110b, and BTS cell controller 114 as shown in Figures 1B and 1C). The hardware utilization, scheduling, and maintenance are under the control of the API. By embedding these mechanisms in the API, a process can be designed in isolation, with the synchronization issues handled at only one level within the software hierarchy. This produces a system that is considerably quicker to build and more efficient in the use of hardware than one that uses many synchronization techniques within the design. RAVI: WRONG: Additional description on the process for providing C-based API programming guide 171 and API functions mapped to instruction set 172 is provided in co-pending patent application entitled “A METHOD FOR ~~DESIGNING~~ DESIGNING A CONFIGURATION FOR A CONFIGURABLE SPREAD SPECTRUM ~~SPECTRUM~~ COMMUNICATION DEVICE,” Attorney Docket No. 9824-083-999, US patent application serial number \_\_\_\_\_, 07/772,582, filed on January 29, 2001, now US patent number 6,701,582. This related application is commonly assigned, and is hereby incorporated by reference.

Replace the paragraph beginning on page 33, line 10, with the following:

Several exemplary hardware kernels have been defined in related co-pending patent applications and are applicable in the present communication device, e.g., 100 of Figure 1b. While these related patent applications provide a specific function for hardware kernels, the present invention is well suited to a wide range of data processing functions for electronic devices, such as a spread spectrum communication device. These commonly assigned and related applications, which are incorporated herein by reference, include:

1) "A CONFIGURABLE CODE GENERATOR SYSTEM FOR SPREAD SPECTRUM APPLICATIONS," Attorney Docket No. 9824-0029-888, US patent application serial number \_\_\_\_\_, 07/751,782, filed on December 2829, 2000, now US patent number 6,567,017;

2) "A CONFIGURABLE MULTIMODE DESREADER FOR SPREAD SPECTRUM APPLICATIONS," Attorney Docket No. 9824-0036-999, US patent application serial number \_\_\_\_\_, 09/751,785, filed on December 2829, 2000;

3) "A CONFIGURABLE ALL-DIGITAL COHERENT DEMODULATOR SYSTEM FOR SPREAD SPECTRUM APPLICATIONS," Attorney Docket No. 9824-0037-999, US patent application serial number \_\_\_\_\_, 09/751,783, filed on December 2829, 2000; and

4) "METHOD AND APPARATUS FOR PROCESSING A SECONDARY SYNCHRONIZATION CHANNEL IN A SPREAD SPECTRUM SYSTEM," Attorney Docket No. 9824-0034-999, US patent application serial number \_\_\_\_\_, 09/772,583, filed on January 29, 2001, filed herewith.